

### Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.43 \text{ V}$  at  $I_F = 10 \text{ A}$ 



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 40 A				
V <sub>RRM</sub>	100 V				
I <sub>FSM</sub>	450 A				
E <sub>AS</sub> at L = 180 mH	700 mJ				
V <sub>F</sub> at I <sub>F</sub> = 40 A	0.64 V				
T <sub>J</sub> max.	150 °C				

### **FEATURES**

- Trench MOS Schottky technology
- Low forward voltage drop, low power losses

• High efficiency operation

COMPLIANT HALOGEN

- Solder dip 275 °C max. 10 s, per JESD 22-B106
- Compliant to RoHS directive 2002/95/EC and in accordance to WEEE 2002/96/EC
- Halogen-free according to IEC 61249-2-21 definition

### **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, dc-to-dc converters and reverse battery protection.

### **MECHANICAL DATA**

Case: TO-3PW

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free and RoHS compliant,

commercial grade

Terminals: Matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: As marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	V80100PW	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	100	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	80	А	
	per diode		40		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	450	А	
Non-repetitive avalanche energy at $T_J = 25$ °C, $L = 180$ r	E <sub>AS</sub>	700	mJ		
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C $\pm$ 2 °C per diode		I <sub>RRM</sub>	1.0	А	
Voltage rate of change (rated V <sub>R</sub> )	dV/dt	10 000	V/µs		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	- 40 to + 150	°C	

## V80100PW

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Breakdown voltage	I <sub>R</sub> = 1.0 mA	T <sub>A</sub> = 25 °C	$V_{BR}$	100 (minimum)	-	V	
Instantaneous forward voltage per diode	I <sub>F</sub> = 10 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub> <sup>(1)</sup>	0.49	-		
	I <sub>F</sub> = 20 A			0.59	-		
	I <sub>F</sub> = 40 A			0.76	0.84		
	I <sub>F</sub> = 10 A	T <sub>J</sub> = 125 °C		0.43	-		
	I <sub>F</sub> = 20 A			0.55	-		
	I <sub>F</sub> = 40 A			0.64	0.76		
Reverse current per diode	V <sub>R</sub> = 80 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	38	-	μΑ	
		T <sub>A</sub> = 125 °C		17	-	mA	
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		85	1000	μΑ	
		T <sub>A</sub> = 125 °C		33	76	mA	

#### **Notes**

 $^{(1)}$  Pulse test: 300  $\mu$ s pulse width, 1 % duty cycle

(2) Pulse test: Pulse width  $\leq$  40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	V80100PW	UNIT		
Typical thermal resistance	per diode	- R <sub>θJC</sub>	1.5	°C/W		
	per device		0.8			

ORDERING INFORMATION (Example)						
PACKAGE PREFERRED P/N U		UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-3PW	V80100PW-M3/4W	4.5	4W	30/tube	Tube	

### **RATINGS AND CHARACTERISTICS CURVES**

 $(T_A = 25 \, ^{\circ}C \text{ unless otherwise noted})$ 

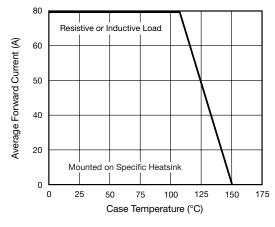


Fig. 1 - Forward Current Derating Curve

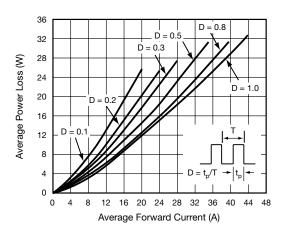


Fig. 2 - Forward Power Loss Characteristics Per Diode



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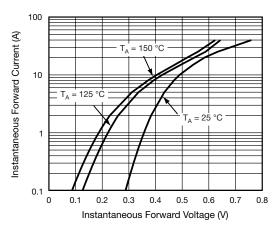


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

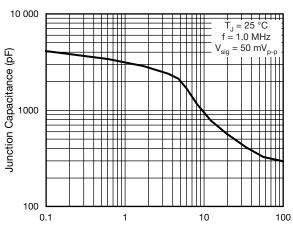


Fig. 5 - Typical Junction Capacitance Per Diode

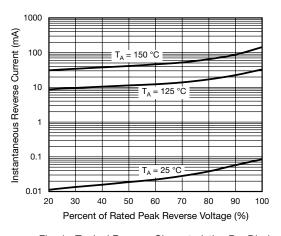


Fig. 4 - Typical Reverse Characteristics Per Diode

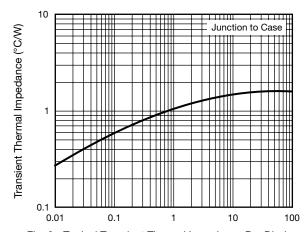
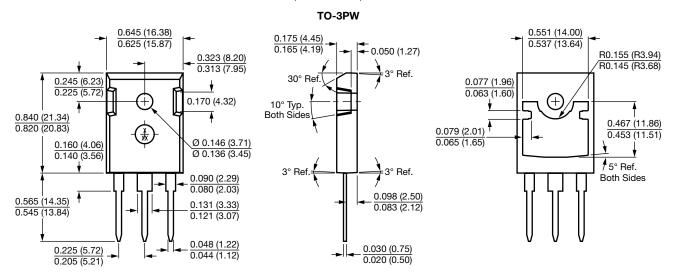


Fig. 6 - Typical Transient Thermal Impedance Per Diode

### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)







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